

**Amendments to the Specification**

On page 1, after the title "Transistor and Semiconductor Devise" please insert the following paragraph:

-- This application is a divisional of US patent application 09/850,732, which is a U.S. national stage application of international application PCT/JP99/06300 filed November 11, 1999, which claims the benefit of Japanese application 10-326889, filed November 17, 1998.

*now US Patent 6,727,522*

*RP  
11/05*

The disclosure of application No. 09/850,732 is incorporated herewith by reference.--